

|   | L #  | Hits | Search Text   | DBs  | Time Stamp           |
|---|------|------|---|--|----------------------|
| 1 | L133 | 20   | ("4019795"  <br>"4740977"   "4818053"<br>  "4942076"  <br>"5029968"   "5055652"<br>  "5479540"  <br>"5542018"   "5638391"<br>  "5837561"  <br>"5847327"   "5852257"<br>  "6081638"  <br>"6146025"   "6191023"<br>  "6205274"  <br>"6219470"   "6229227"<br>  "6249434"  <br>"6301279"  <br>"2003/0159280"  <br>"2003/0159844"  <br>"2003/0161026"  <br>"2003/0161133"  <br>"2003/0161363"  <br>"2003/0161593"  <br>"2003/0161603"  <br>"2003/0174454") .PN. | USPA<br>T                                      | 2004/10/1<br>6 12:58 |
| 2 | L136 | 6    | (("5542018") or<br>("5638391") or<br>("6081638") or<br>("6205274") or<br>("6229227") or<br>("6282005")) .PN.  | USPA<br>T;<br>US-P<br>GPUB<br>;<br>EPO;<br>JPO | 2004/10/1<br>6 13:16 |

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|----|------|---|------------------------------|---------------------|
| 1  | 1    | "20030174454"   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>15:35 |
| 2  | 1    | "20030174454" and header same<br>hybrid adj2 subassembly same<br>driver same trench   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>15:47 |
| 3  | 7    | ((("5542018") or ("5638391") or<br>("6081638") or ("6205274") or<br>("6559227") or ("6282005") or<br>("6638391"))).PN.                                      | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>15:50 |
| 4  | 128  | laser and (driver or<br>control\$4) and (submount or<br>subassembly) and trench   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:06 |
| 5  | 28   | laser same trench same driver   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:10 |
| 6  | 45   | chip same trench same driver  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:10 |
| 7  | 42   | (chip same trench same driver)<br>not (laser same trench same<br>driver)  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:11 |
| 8  | 6    | ((chip same trench same<br>driver) not (laser same trench<br>same driver)) and laser  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:14 |
| 9  | 2080 | (372/34,36).CCLS.   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:15 |
| 10 | 18   | ((372/34,36).CCLS.) and trench<br>and chip  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:19 |
| 11 | 24   | ((372/34,36).CCLS.) and trench  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:19 |
| 12 | 6    | ((372/34,36).CCLS.) and<br>trench) not<br>(((372/34,36).CCLS.) and<br>trench and chip )   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:20 |
| 13 | 29   | (laser or LD adj chip) with<br>trench same (driver or<br>driving)   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:34 |
| 14 | 43   | (laser or LD adj chip) same<br>trench same (driver or<br>driving)   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:34 |
| 15 | 288  | (laser or LD adj chip) same<br>trench same (driver or driving<br>or control\$4)   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:40 |
| 16 | 259  | ((laser or LD adj chip) same<br>trench same (driver or driving<br>or control\$4) ) not ((laser or<br>LD adj chip) with trench same<br>(driver or driving) ) | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>16:35 |
| 17 | 44   | (laser or LD adj chip) with<br>trench and (driver or driving)<br>adj2 circuit\$3  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:17 |
| 18 | 74   | (laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with separate\$   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:17 |

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|----|------|---|------------------------------|---------------------|
| 19 | 10   | (laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with separate\$ and<br>(submount or mount)  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:19 |
| 20 | 5    | (laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 and<br>(submount or mount)   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:20 |
| 21 | 29   | (laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/10/16<br>13:15 |
| 22 | 24   | ((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 ) not<br>((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 and<br>(submount or mount))  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:20 |
| 23 | 188  | (laser or LD adj chip) and<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:23 |
| 24 | 164  | ((laser or LD adj chip) and<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 ) not<br>(((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 ) not<br>((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 and<br>(submount or mount)))                                | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:24 |
| 25 | 116  | ((((laser or LD adj chip) and<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 ) not<br>(((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 ) not<br>((laser or LD adj chip) with<br>(driver or driving) adj2<br>circuit\$3 with isolat\$4 and<br>(submount or mount)))) and<br>(heat or thermal\$4) | USPAT; US-PGPUB;<br>EPO; JPO | 2004/07/24<br>17:24 |
| 26 | 176  | laser same trench same thermal  | USPAT; US-PGPUB;<br>EPO; JPO | 2004/10/15<br>18:35 |
| 27 | 977  | laser same gap same thermal   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/10/15<br>18:35 |
| 28 | 108  | laser same air adj2 gap same<br>thermal   | USPAT; US-PGPUB;<br>EPO; JPO | 2004/10/15<br>18:50 |